



# MORPHEUS

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## D4.7: MORPHEUS Demonstration Board

CONTRACT NO	MORPHEUS IST 027342
TYPE OF DOCUMENT	Publishable abstract of D4.7
DATE	10/09/2009
ABSTRACT	This document is the abstract of the D4.7. It is available on the MORPHEUS public website
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WORKPACKAGE	WP7
CONFIDENTIALITY LEVEL	PU
FILING CODE	MORPHEUS-DTO-D4 7-R1 6.doc

## Context

This deliverable is part of the MORPHEUS project which is a european initiative financed under the 6th FP and addresses innovative solutions for embedded computing based on dynamically reconfigurable platform and tools.

MORPHEUS project aims at satisfying embedded systems new demanding requirements in terms of computing performance, cost-efficient development, functional flexibility and sustainability by developing a global solution based on a modular heterogeneous SOC platform providing dynamically reconfigurable computing completed by a software oriented design flow and a consistent toolset.

MORPHEUS is a 3-year project started in 2006 and gathering all the required expertises from several countries : academics, industrials, SMEs.

## Aim of the deliverable

The aim of the deliverable is the presentation and documentation of the MORPHEUS demonstration board that uses the MORPHEUS IC.

## Content of the deliverable

In this deliverable the MORPHEUS demo board is presented and described. Since it is intended to use this document as user guide for the MORPHEUS demonstration board it describes pragmatically how to set up the board, configure the MORPHEUS demo chip and how to upload and start a small application onto the board.

This delivery describes the main features of the MORPHEUS board. The board features

- a PGA208 ZIF socket for the MORPHEUS IC
- an on-board power supply
- clock-circuitry
- two RS232 interfaces
- an Ethernet Phy and connector
- Extension Board Connector
- general purpose IOs
- SRAM and SRAM debug connector
- status LEDs
- reset buttons
- configuration switches

The purpose of this board is to enable the MORPHEUS project partners to implement their WP5 applications on the MORPHEUS IC. Applications are uploaded in a two-step procedure described in detail in the MORPHEUS document MORPHEUS-ST-D4.6-R1.2.